

ABSTRACT

Aspects for collision recovery interface support in a home phoneline networking alliance (HPNA) control chip are described. The aspects include providing transmit data path logic to receive and transmit data packets within the HPNA control chip. The transmit data path logic is consolidated to include a transmit state machine that handles interfacing the transmit data path logic to at least two separate collision recovery logic means of the HPNA control chip through a minimal number of generic interface signals.